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#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re	Application of:	)		DECE	N/ED	
Hiroyuki Nitta et al.		) )	Group Art Unit: 2815	RECEIVED JUN 2 5 2004 OFFICE OF PETITIONS		
Serial No.: 09/883,210 )		) }	Examiner: J. Nguyen			
Filed:	June 19, 2001	į	Atty. Dkt. No. 01701.00086		•	
For:	Semiconductor Device Having A Wiring Layer Of Damascene Structure And Method For Manufacturing The Same	)	This paper of 1 page is being faxed to 3 on April 19, 2002.  Christopher R. Glembocki Reg. No. 38,800		#12 Election	
Manufacturing The Same  Reg. No. 38,800  RESPONSE TO ELECTION REQUIREMENT  SP. (1975)						
Assistant Commissioner of Patents						

Sir:

Washington, D.C. 20231

This paper is responsive to the Restriction Requirement mailed February 7, 2002, and to the Holding of Applicants response as non-responsive of March 22, 2002. This paper is filed within one month of the March 22, 2002, communication.

Applicants hereby elect Species V, relating to Figures 6A-6E, without traverse. Claims 1-4 and 10-15 correspond to Species V.

Applicant reserves the right to file a divisional application directed to the non-elected invention. A preliminary amendment is filed herewith under separate cover. Please charge any fee associated with the filing of this paper to our Deposit Account No. 19-0733.

Respectfully submitted,

Joseph M. Potenza

Registration No. 28,175

BANNER & WITCOFF, LTD. 1001 G Street, N.W., 11<sup>th</sup> Floor Washington, D.C. 20001 (202) 508-9100

Dated:

April 19, 2002

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# **OFFICIAL**

**PATENT** 

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For: Semiconductor Device Having A Wiring Layer Of Damascene Structure And Method For Manufacturing The Same	This paper of 3 pages is being faxed to 703 308 7722 on April 19-2002. Christopher R. Glembocki Reg. No. 38,800

#### **AMENDMENT**

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

Please amend the application as follows:

## In the Claims:

Please amend claim 4 as follows:

- 4. (Amended) The semiconductor device according to claim 1, further comprising:
- a second insulation film formed above the space region, the second insulation film being different from the first insulation film; and
- a third insulation film formed on a sidewall of the groove section, the third insulation film being different from the first and second insulation films.

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**PATENT** 

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Sir:

Please amend the application as follows:

## In the Claims:

Please amend claim 4 as follows:

- 4. (Amended) The semiconductor device according to claim 1, further comprising:
- a second insulation film formed above the space region, the second insulation film being different from the first insulation film; and